

EAST-7849.wsp:1

File View Edit Tools Window Help

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval C	Inventor	S	C	P	2	3
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20040150886	20040819	40	Method of manufacturing a semiconductor integrated circuit	257/331			Lee, Sang-Eun et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20040150886	20040812	52	Methods of forming polymer films and of using such films	438/758			Xu, Songlin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20040150886	20040812	53	Methods of using polymer films to form micro-devices	438/694			Xu, Songlin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20040150886	20040708	25	MOS TRANSISTOR	438/182			Doris, Bruce B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20040150886	20040520	10	SIGE VERTICAL GATE CONTACT FOR GATE COM	438/270			Hummeler, Klaus	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input type="checkbox"/>	US 20040097070	20040506	18	Process for integration of a high dielectric constant oxide in	438/199	257/E21.633;		Wang, Ming-Fang et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input type="checkbox"/>	US 20040097075	20040506	14	Semiconductor devices having contact plugs and local	257/296	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input type="checkbox"/>	US 20040097074	20040401	6	Method for producing a semiconductor component	438/585	257/E21.407;		Behammer, Dag	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input type="checkbox"/>	US 20040097073	20040311	13	Source and drain elements	117/84	257/E21.452;		Langdo, Thomas A. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/>	US 20030160340	20030828	14	Semiconductor device having increased metal silicide partition	438/233	257/E21.634;		Wieczorek, Karsten et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/>	US 20030160340	20030529	23	Novel MOS transistor structure and method of fabric	257/288	257/E21.634;		Murthy, Anand et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/>	US 20030045061	20030306	11	METHOD OF FORMING A SPACED	438/303	257/E21.435;		Kwon, Hyung-Shin et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	<input type="checkbox"/>	<input type="checkbox"/>	US 20030045061	20030116	15	METHOD OF FABRICATING NO SPAC		257/E21.435;		Deshpande, Sadanand et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14	<input type="checkbox"/>	<input type="checkbox"/>	US 20020011090	20021219	17	Field emitter cell and array with vertical thin film edges	313/309	257/E21.641;		Hsu, David S.Y. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15	<input type="checkbox"/>	<input type="checkbox"/>	US 20020011090	20021219	23	NOVEL MOS TRANSISTOR STRUCTURE AND METHOD	257/286	257/240		MURTHY, ANAND et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
16	<input type="checkbox"/>	<input type="checkbox"/>	US 20020011090	20021205	14	Semiconductor devices having contact plugs and local	438/618	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
17	<input type="checkbox"/>	<input type="checkbox"/>	US 20020011090	20021205	14	SEMICONDUCTOR DEVICES HAVING CONTACT	438/241	257/E21.507;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
18	<input type="checkbox"/>	<input type="checkbox"/>	US 20020011090	20020502	11	Method of fabricating cell of non-volatile device	438/262	257/E21.682;		Lee, Seong-Soo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval C	Inventor	S	C	P	3
19	<input type="checkbox"/>	<input type="checkbox"/>	US 6811227	20011227	29	Methods of forming	438/183	257/E21.444;		Uh, Hyung-Soo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
20	<input type="checkbox"/>	<input type="checkbox"/>	US 6810906	20010906	16	Semiconductor device and	257/317	257/E21.682		Meguro, Hisataka	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
21	<input type="checkbox"/>	<input type="checkbox"/>	US 6810726	20010726	16	Method of making insulator	438/595	257/E21.358;		Mouli, Chandra V. et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
22	<input type="checkbox"/>	<input type="checkbox"/>	US 6810726	20010726	18	Reduced gate length transistor	438/302	257/E21.345;		Bothra, Subhas et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
23	<input type="checkbox"/>	<input type="checkbox"/>	US 6812103	20041102	21	Methods of fabricating a	438/300	438/302;		Wang, Hongmei et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
24	<input type="checkbox"/>	<input type="checkbox"/>	US 6797556	20040928	23	MOS transistor structure and	438/231	257/288;		Murthy, Anand et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
25	<input type="checkbox"/>	<input type="checkbox"/>	US 6790717	20040914	6	Method for producing a	438/182	257/309;		Behammer, Dag	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
26	<input type="checkbox"/>	<input type="checkbox"/>	US 6780694	20040824	24	MOS transistor	438/182	257/133;		Doris, Bruce B. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
27	<input type="checkbox"/>	<input type="checkbox"/>	US 6764966	20040720	12	Spacers with a graded	438/785	438/578;		En, William George et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
28	<input type="checkbox"/>	<input type="checkbox"/>	US 6700205	20040302	11	Semiconductor devices	257/776	257/300;		Tang, Sanh D. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
29	<input type="checkbox"/>	<input type="checkbox"/>	US 6673665	20040106	14	Semiconductor device having	438/233	257/E21.634;		Wieczorek; Karsten et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
30	<input type="checkbox"/>	<input type="checkbox"/>	US 6656764	20031202	16	Process for integration of a	438/104	257/E21.633;		Wang, Ming-Fang et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
31	<input type="checkbox"/>	<input type="checkbox"/>	US 6627504	20030930	8	Stacked double sidewall	438/303	438/267;		Bertrand; Jacques J. et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
32	<input type="checkbox"/>	<input type="checkbox"/>	US 6613621	20030902	27	Methods of forming	438/183	257/E21.444;		Uh, Hyung-Soo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
33	<input type="checkbox"/>	<input type="checkbox"/>	US 6595820	20030722	16	Field emitter cell and array	445/24	313/309		Hsu, David S. Y. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
34	<input type="checkbox"/>	<input type="checkbox"/>	US 6576507	20030610	10	Selectively removable filler	438/234	257/E21.696;		Bandy, Kenneth A. et	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
35	<input type="checkbox"/>	<input type="checkbox"/>	US 6573139	20030603	12	Method of fabricating cell of	438/257	257/315;		Lee, Seong-soo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
36	<input type="checkbox"/>	<input type="checkbox"/>	US 6573136	20030603	10	Isolating a vertical gate	438/242	257/E21.649;		Hummeler, Klaus	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

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	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XR	Retrieval C	Inventor	S	C	P	3	4
55			US 6159065 A	20001212	15	Method for manufacturing a monocrystalline thin film display device	445/24	445/23		Jung, Sung Jae et al.					
56			US 6124177 A	20000926	9	Method for making deep sub-micron mask structures	438/305	257/410; 257/522		Lin; Chrong Jung et al.					
57			US 6107141 A	20000822	11	Flash EEPROM	438/267	438/257		Hsu; Louis Lu-Chen et al.					
58			US 6084245 A	20000704	16	Field emitter cell and array with vertical thin film edges	257/10	313/309; 212/310		Hsu; David S. Y. et al.					
59			US 6074921 A	20000613	25	Self-aligned processing of semiconductor device features	438/299	257/E21.31; 257/E21.425		Lin; Xi-Wei					
60			US 5953612 A	19990914	26	Self-aligned silicidation technique to independently form dual sidewall contacts	438/299	257/E21.004; 257/E21.100		Lin; Xi-Wei et al.					
61			US 5910912 A	19990608	12	Flash EEPROM with dual sidewall contacts	365/185.01	257/321; 257/E21.423		Hsu; Louis Lu-Chen et al.					
62			US 5904508 A	19990518	16	Semiconductor device and a method of manufacturing the same	438/151	257/E21.413; 257/E21.414		Codama; Mitsufumi et al.					
63			US 5736435 A	19980407	8	Process for fabricating a fully self-aligned device	438/151	257/E21.415; 257/E21.403		Venkatesan; Suresh et al.					
64			US 5619097 A	19970408	76	Panel display with dielectric spacer structure	313/495	313/309; 212/326		Jones, Gary W.					
65			US 5587623 A	19961224	75	Field emitter structure and method of forming the same	313/497	313/309; 212/326		Jones, Gary W.					
66			US 5548181 A	19960820	78	Field emission device comprising dielectric overlayer	313/309	313/495; 212/558		Jones, Gary W.					
67			US 5529524 A	19960625	78	Method of forming a spacer structure between conductive surfaces	445/24			Jones, Gary W.					
68			US 5298444 A	19940329	8	Method for manufacturing a field effect transistor	438/571	257/213; 257/E21.026		Ristow; Dietrich					
69			US 5220186 A	19930615	12	Semiconductor device with a mushroom-shaped gate electrode	257/284	257/280; 257/429		Kasai; Nobuyuki et al.					
70			US 5089434 A	19920218	18	Mask surrogate semiconductor process employing a topographic pattern	438/274	257/E21.033; 257/E21.018		Hollinger, Theodore G.					
71			US 5045903 A	19910903	22	Delayed carrier MOSFET	257/339	257/341; 257/401		Meyer, Theodore O. et al.					
72			US 4932693 A	19900612	10	Gate latch assembly and hinge	292/205	16/299; 16/573		Schwartz, Bruce H.					